

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <b>10-053104</b>	GROUP/UNIT <b>2817</b>	ATTACHMENT TO PAPER NUMBER <b>7</b>	
NOTICE OF REFERENCES CITED				APPLICANT(S) <b>Yang et al.</b>			
<b>U.S. PATENT DOCUMENTS</b>							
*	DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE	
A	<b>651 2419</b>	<b>1-28-2003</b>	<b>Adams et al.</b>	<b>331</b>	<b>179</b>		
B							
C							
D							
E							
F							
G							
H							
I							
J							
K							
<b>FOREIGN PATENT DOCUMENTS</b>							
*	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB- CLASS	PERTINENT SHTS. PP. DWG. SPEC.
L	<b>EP 143251 A2</b>	<b>1-7-1992</b>	<b>European</b>	<b>Saito et al.</b>	<b>—</b>	<b>—</b>	
M							
N							
O							
P							
Q							
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
R	<b>Lin "A 900MHz, 2.5mA CMOS Frequency Synthesizer with an Automatic SC Tuning Loop" Electrical Engineering Dept. University of California, Los Angeles Mg 21-24 7000 pp 375-378</b>						
S							
T							
U							
EXAMINER		DATE					
<b>MICHAEL B SHINGLETON</b>		<b>4-23-03</b>					
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)							